

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination	
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		Examiner	Art Unit	Page 1 of 1
		Ryan Lepisto	2883	

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"Input Coupling and Guided-wave Distribution Schemes for Board-level Intra-chip Guided-Wave Optical Clock Distribution Network Using Volume Grating Coupler Technology", Mule et al, Proceedings of the IEEE, June 2001, p. 128-130.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.